

366/572

257	356
Class	Subclass
ISSUE CLASSIFICATION	



PATENT NUMBER
1

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
<i>Ed 2 on Am</i>	<i>11/11</i>

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/876292		257	344	285	<i>Ed 2 on Am</i>

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TITLE
Additional n-type LDD/pocket implant for improving short-channel NMOS ESD robustness

PTO-3240
1298

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
257	356		325	330	339	344	345	347	
INTERNATIONAL CLASSIFICATION		257	348	349	355	356	357	358	
H01L	23/02	257	361	362					
		438	282	292	910				

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	<i>Chengdu City</i> 3/3 (Assistant Examiner) (Date)			11 1	
	(Primary Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	(Legal Instruments Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
			ISSUE BATCH NUMBER		

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